

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**RECEIVED**
CENTRAL FAX CENTER

JUN 29 2004

Applicant(s): Michael H. Perrott

Title: METHOD AND APPARATUS FOR BIT ERROR RATE DETECTION

Application No.: 09/888,708

Filed: June 25, 2001

Examiner: Lamarre, Guy J.

Group Art Unit: 2133

Atty. Docket No.: 026-0015

Confirmation No.: 8814

OFFICIAL

June 29, 2004

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450**RESPONSE AFTER FINAL REJECTION (37 C.F.R. § 1.116)**

This paper is being submitted following the Final Rejection mailed on April 29, 2004. In light of the Amendments and Remarks herein, further consideration is requested.